

07-01-02-

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Rajiv Jain and Richard J. Wong

Assignee:

QuickLogic Corporation

Title:

Method of Programming an Antifuse

Serial No.:

09/887,834

Filing Date:

June 22, 2001

Examiner:

T. Cunningham

Group Art Unit:

2816

Docket No.:

M-9351 US

San Jose, California June 26, 2002

COMMISSIONER FOR PATENTS Washington, D. C. 20231

SUPPLEMENTAL RESPONSE TO OFFICE ACTION

Dear Sir:

This is a supplemental response to the Office Action with a mailing date of March 27, 2002. The Applicants have previously responded on June 25, 2002.

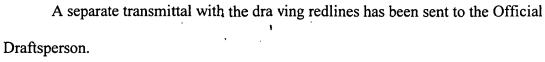
The Examiner has objected to the drawings under 37 C.F.R. 1.83(a), as not showing every feature of the invention specified in the claims. The Examiner made specific reference to the voltages of claims 19 and 23. The Examiner has also referred to claim 26. Applicants point out that no claim 26 is pending in the application.

The Applicants propose the following drawing corrections. Figures 2 and 5 are proposed to be corrected to address the Examiner's concern. No new matter is added with these changes. The correction to Figure 2 is supported at least by the paragraph beginning on page 5, line 18, and the paragraph beginning on page 6, line 3. The correction to Figure 4 is supported at least by the paragraph beginning on page 8, line 11.

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> > Serial No. 09/887,834



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Should the Examiner have any questions regarding this response, the Examiner is invited to call the undersigned at (408) 453-9200.

EXPRESS MAIL LABEL NO:

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Respectfully submitted,

Norman R. Klivans Attorney for Applicants

Reg. No. 31,003

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